

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P O Box (450) Alexandria, Virginia 22313-1450 www.upto.gov

BIB DATA SHEET

CONFIRMATION NO. 5751

| SERIAL NUMBER FILING OF | | | | CLASS | GROUP ART UNIT | | ATTORNEY DOCKET | | | | |
|---|---|----------|------------------------|-------|---------------------|---------|----------------------|-------------|--------------------------|-----------------------|--|
| 10/612,22 | 2 | 07/03/2 | | | 033 | | 3726 | | 300.1119 | | |
| | | RUL | E | | | | | | | | |
| APPLICANTS Masatoshi Akagawa, Nagano-shi, JAPAN; Kazunari Sekigawa, Nagano-shi, JAPAN; Shirichi Wakabayashi, Nagano-shi, JAPAN; ***CONTINUING DATA | | | | | | | | | | | |
| ** FOREIGN APPLICATIONS ************************************ | | | | | | | | | | | |
| JAPAN 2002-20055(PAT. 09/07/2002 JAPAN 2002-352440(PAT. 12/04/2002 | | | | | | | | | | | |
| ** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/29/2003 | | | | | | | | | | | |
| Foreign Priority claims | | Yes No | ☐ Metat | ter | STATE OR COUNTRY | | HEETS | TOT | | INDEPENDENT CLAIMS | |
| 35 USC 119(a-d) conditions met Yes No Verified and /RICK KILTAE | | | Met after Allowance | | JAPAN | DRA | | | MS CLAIMS → 32 | | |
| Acknowledged CHANG/ Examiner's Signature | | Tritials | | JAFAN | | 10 "496 | | , <u>ee</u> | | | |
| ADDRESS | | | | | | | | | | | |
| STAAS & HALSEY LLP SUITE 700 1201 NEW YORK AVENUE, N.W. WASHINGTON, DC 20005 UNITED STATES | | | | | | | | | | | |
| TITLE | | | | | | | | | | | |
| Component-embedded board fabrication method | | | | | | | | | | | |
| | | | | | | | ☐ All Fees | | | | |
| | | | | | | | ☐ 1.16 Fees (Filing) | | | | |
| | FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT | | | | | | | ees (Pr | Processing Ext. of time) | | |
| | Nofor following: | | | | | | ☐ 1.18 Fees (Issue) | | | | |
| | | | | | | | ☐ Other | | | | |
| | | | | | | | ☐ Credit | | | | |